



PM1360

High Current, Power Inductors



Description

- Halogen Free
- 125°C maximum total temperature operation
- Surface mount package
- Powder iron core material
- Magnetically shielded, low EMI
- High current carrying capacity, Low core losses
- RoHS compliant

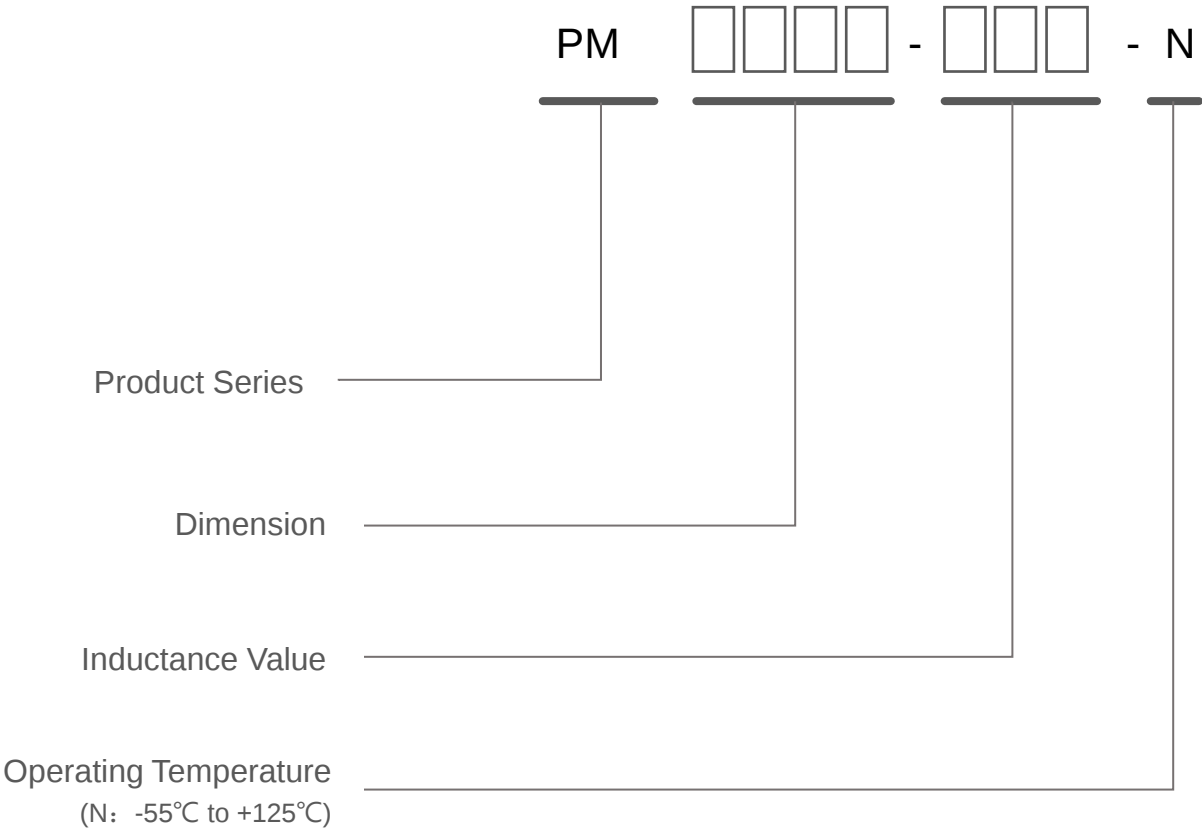
Application

- Voltage Regulator Module (VRM)
- Multi-phase regulators,
- Point-of-load module, Smart phone POL modules
- SSD modules, Notebook regulators
- Battery power systems
- Graphics cards
- Data networking and storage systems
- DC/DC converter
- Cellular phones, LCD displays, HDDs

Environmental Data

- Storage temperature range: -55°C to +125 °C
- Operating temperature range: -40°C to +125°C
(ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020D compliant

Ordering Information



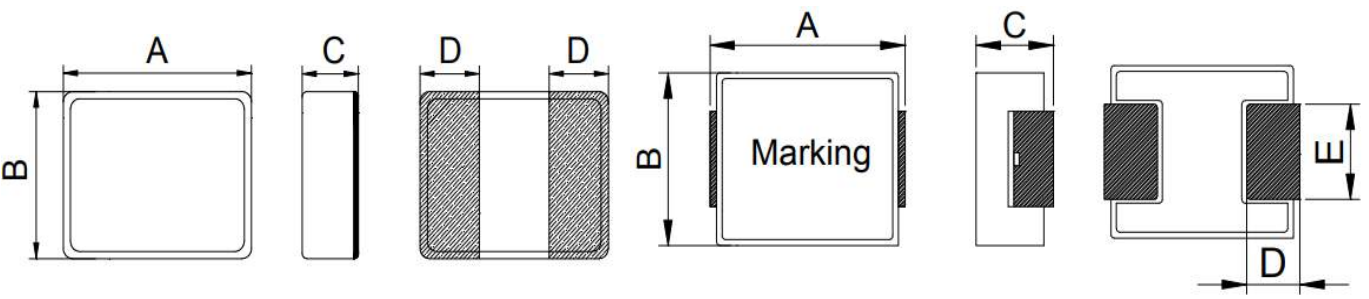
Product Specifications

Part No.	Inductance	DC Resistance		Heating Rating Current	Saturation Current
	L0 (μH)	DCR (mΩ)		Idc (A)	Isat (A)
	± 20 %	TYP.	MAX.	TYP.	TYP.
PM1360-R33-N	0.33	0.58	0.8	43.0	65.0
PM1360-1R0-N	1.0	1.4	1.7	24.0	35.0
PM1360-1R5-N	1.5	2.5	4.0	22.0	31.0
PM1360-2R2-N	2.2	4.2	6.0	18.0	26.0
PM1360-3R3-N	3.3	5.6	9.0	12.0	23.0
PM1360-4R7-N	4.7	7.2	10.5	11.5	18.0
PM1360-6R8-N	6.8	10.0	13.8	11.5	15.0
PM1360-8R2-N	8.2	13.6	16.0	11.0	13.5
PM1360-100-N	10	18.0	20.7	10.0	12.5
PM1360-150-N	15	25.0	29.0	6.0	9.0
PM1360-180-N	18	30.0	35.0	5.0	8.0
PM1360-220-N	22	34.0	39.5	5.0	7.5
PM1360-270-N	27	54.0	60.0	4.0	6.5
PM1360-330-N	33	65.0	75.0	4.0	6.0
PM1360-470-N	47	80.0	90.0	3.5	5.5
PM1360-680-N	68	115.0	130.0	3.3	4.5
PM1360-820-N	82	120.0	140.0	3.0	4.0
PM1360-101-N	100	180.0	200.0	2.5	3.5
PM1360-121-N	120	210.0	235.0	2.3	3.2
PM1360-151-N	150	300.0	350.0	2.0	2.7

Notes

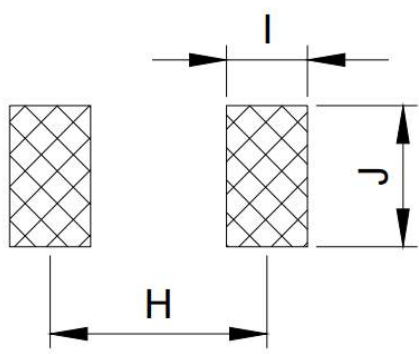
1. Initial Inductance (L0) Test Parameters:1MHz,1V,Idc=0.0A,+25°C
2. Operating temperature range - 40 °C to + 125 °C
3. IDC(A): DC current (A) that will cause an approximate ΔT of 40 °C
4. ISAT(A): DC current (A) that will cause L0 to drop approximately 30 %
5. The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
6. The rated current as listed is either the saturation current or the heating current depending on which value is lower.

Dimensions



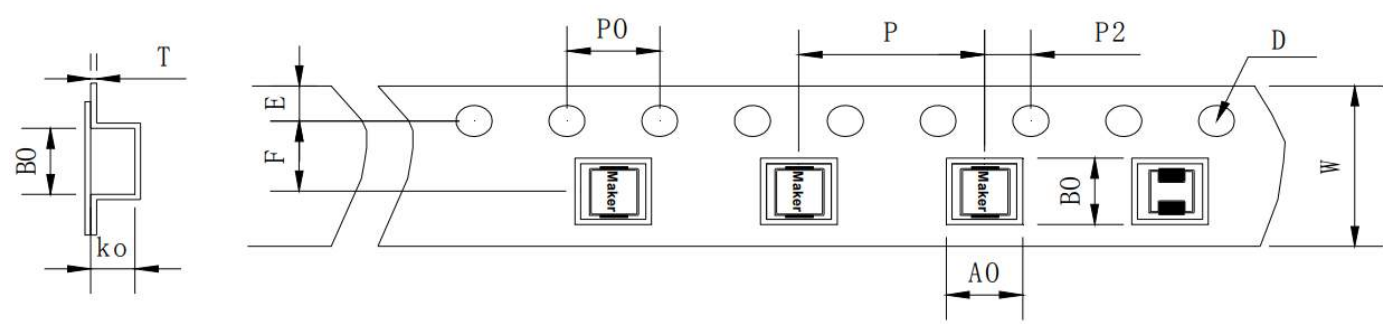
Part No.	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
PM1360-XXX-N	13.8 Max.	12.6±0.3	6.0 Max	2.7±0.7	3.0±0.5/3.5±0.5

RECOMMENDED PCB LAYOUT



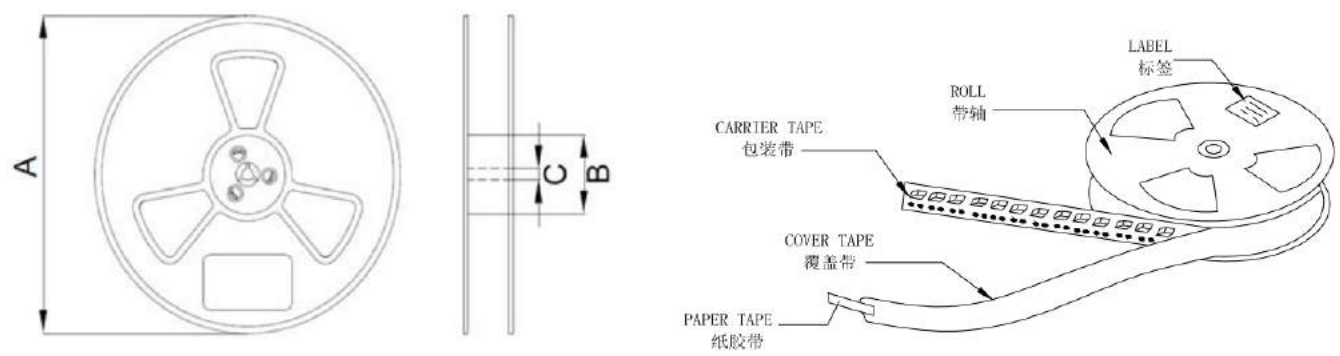
Part No.	H(mm)	I(mm)	J(mm)
PM1360-XXX-N	10.5	4	5.5

Tape Dimensions



Part No.	Tape Dimensions(mm)					
	W	A0	B0	K0	D	P0
PM1360-XXX-N	24	13.2	13.4	6.8	1.5	20

Reel Dimensions



Part No.	Reel Dimensions(mm)		
	A	B	C
PM1360-XXX-N	330	100	13

Numbers of taping

400 pieces/reel

Reflow Profile

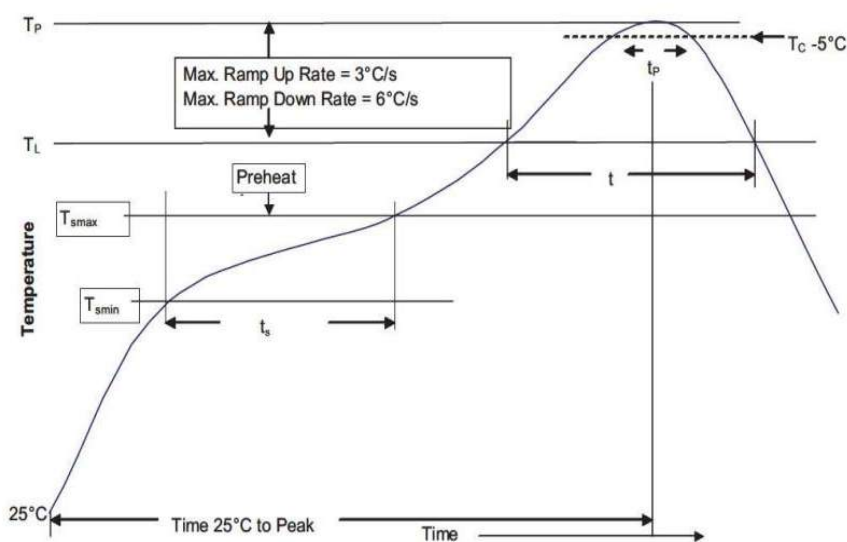


Table 1 - Standard SnPb Solder (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5mm	235°C	220°C
≥2.5mm	220°C	220°C

Table 2 - Lead (Pb) Free Solder (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350 - 2000	Volume mm ³ >2000
<1.6mm	260°C	260°C	260°C
1.6 – 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

Profile Feature	Standard SnPb Solder	Lead (Pb) Free Solder
Preheat and Soak		
• Temperature min. (T_{smin})	100°C	150°C
• Temperature max. (T_{smax})	150°C	200°C
• Time (T_{smin} to T_{smax}) (t_s)	60-120 Seconds	60-120 Seconds
Average ramp up rate T_{smax} to T_p	3°C/ Second Max.	3°C/ Second Max.
Liquidous temperature (T_L)	183°C	217°C
Time at liquidous (t_L)	60-150 Seconds	60-150 Seconds
Peak package body temperature (T_p)*	Table 1	Table 2
Time (t_p)** within 5 °C of the specified classification temperature (T_c)	20 Seconds**	30 Seconds**
Average ramp-down rate (T_p to T_{smax})	6°C/ Second Max.	6°C/ Second Max.
Time 25°C to Peak Temperature	6 Minutes Max.	8 Minutes Max.



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